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APPLICATION NO.	F	ILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/073,999 02/14/2002		02/14/2002	. Ken Takeuchi	001701.00140	9741	
22907	22907 7590 10/14/2005				EXAMINER	
BANNER &		-	HO, HO	HO, HOAI V		
1001 G STR SUITE 1100		,	ART UNIT	PAPER NUMBER		
WASHINGT	ON, DC	20001	2827			
				DATE MAILED: 10/14/2005		

Please find below and/or attached an Office communication concerning this application or proceeding.

## **Advisory Action**

Application No.	Applicant(s)	
10/073,999	TAKEUCHI ET AL.	
Examiner	Art Unit	
Hoai V. Ho	2827	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 14 September 2005 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE.

Therefo inal rej conditio	ore, further action by the applicant is required to avoid abandonment of this applicati jection under 37 CFR 1.113 may only be either: (1) a timely filed amendment which on for allowance; (2) a timely filed Notice of Appeal (with appeal fee); or (3) a timely nation (RCE) in compliance with 37 CFR 1.114.	on. A proper reply to a places the application in
	PERIOD FOR REPLY [check either a) or b)]	
	The period for reply expiresmonths from the mailing date of the final rejection.  The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing of ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE 706.07(f).	date of the final rejection.
ee have ee unde 2) as se	ensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR been filed is the date for purposes of determining the period of extension and the corresponding amouner 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply or to the form in (b) above, if checked. Any reply received by the Office later than three months after the mailined, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).	nt of the fee. The appropriate extension iginally set in the final Office action; or
	A Notice of Appeal was filed on Appellant's Brief must be filed within the peri 37 CFR 1.192(a), or any extension thereof (37 CFR 1.191(d)), to avoid dismissal of the second sec	
2.	The proposed amendment(s) will not be entered because:	
(a)	they raise new issues that would require further consideration and/or search (se	ee NOTE below);
(b)	☐ they raise the issue of new matter (see Note below);	
(c)	they are not deemed to place the application in better form for appeal by material issues for appeal; and/or	ally reducing or simplifying the
(d)	they present additional claims without canceling a corresponding number of fin NOTE:	ally rejected claims.
3. 🗌 🛭 A	Applicant's reply has overcome the following rejection(s):	
	Newly proposed or amended claim(s) would be allowable if submitted in a sep canceling the non-allowable claim(s).	arate, timely filed amendment
	The a) affidavit, b) exhibit, or c) request for reconsideration has been consideration in condition for allowance because:	ered but does NOT place the
	The affidavit or exhibit will NOT be considered because it is not directed SOLELY to raised by the Examiner in the final rejection.	issues which were newly
	For purposes of Appeal, the proposed amendment(s) a)  will not be entered or b) explanation of how the new or amended claims would be rejected is provided below	
-	The status of the claim(s) is (or will be) as follows:	
	Claim(s) allowed:	
	Claim(s) objected to:	
	Claim(s) rejected: <u>47-60,62,63,65 and 66</u> .	
	Claim(s) withdrawn from consideration:	•
8.	The proposed drawing correction filed on is a)☐ approved or b)☐ disappro	oved by the Examiner.
9. <u> </u>	Note the attached Information Disclosure Statement(s)( PTO-1449) Paper No(s)	·
10.🛛	Other: <u>See Continuation Sheet</u>	Mill
		Hoai V. Ho Primary Examiner Art Unit: 2827

Continuation of 10. Other: The proposed amendment has been entered but does NOT place the application in condition for allowance because: Figure 13 of Hemink shows a common latch circuit (103) connects to one ends of the first and second bit lines (a signal line connects to a switch S2 of a lower and upper block in fig. 8). Figure 38 of Sakui shows a common latch circuit (SENSE AMPLIFIER) connects to BLi and /BLi.